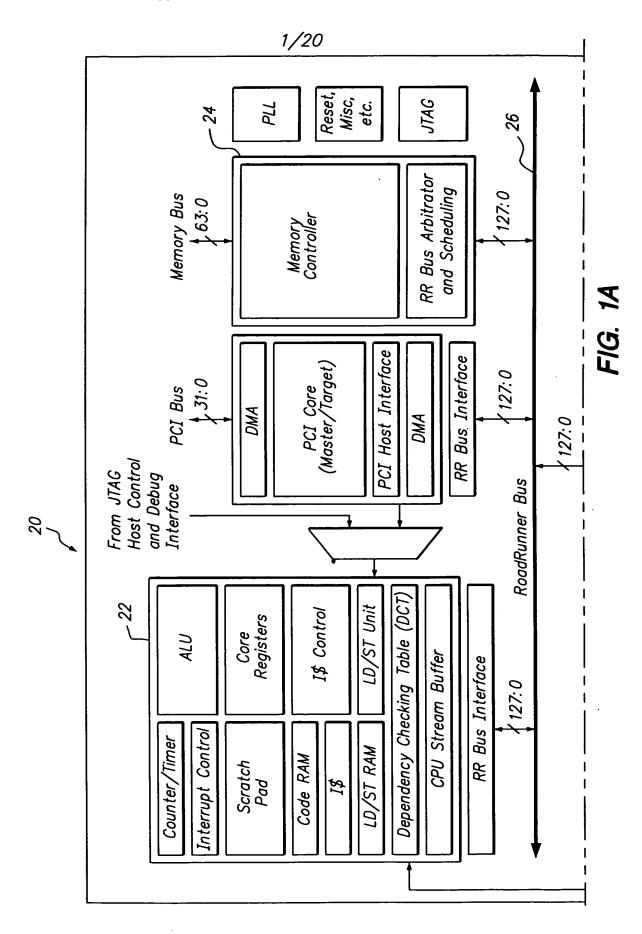
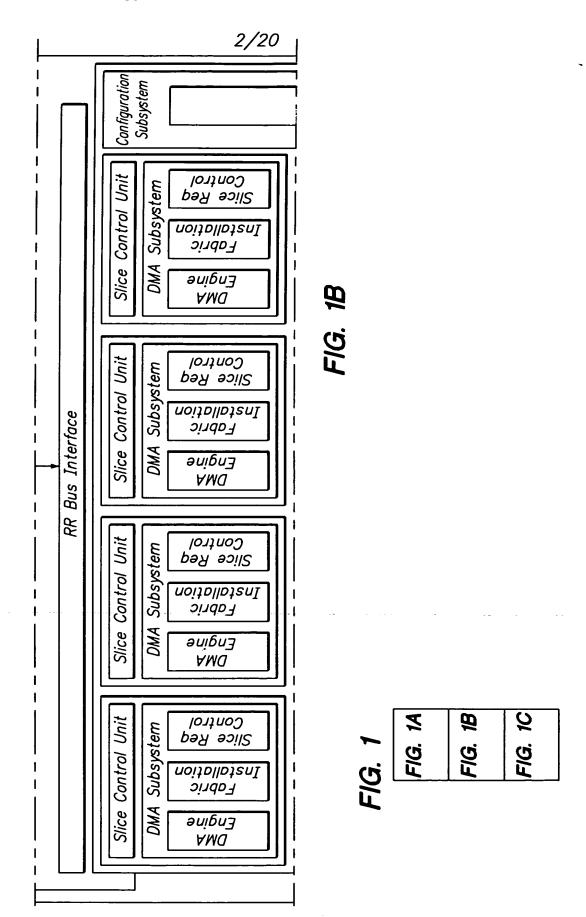
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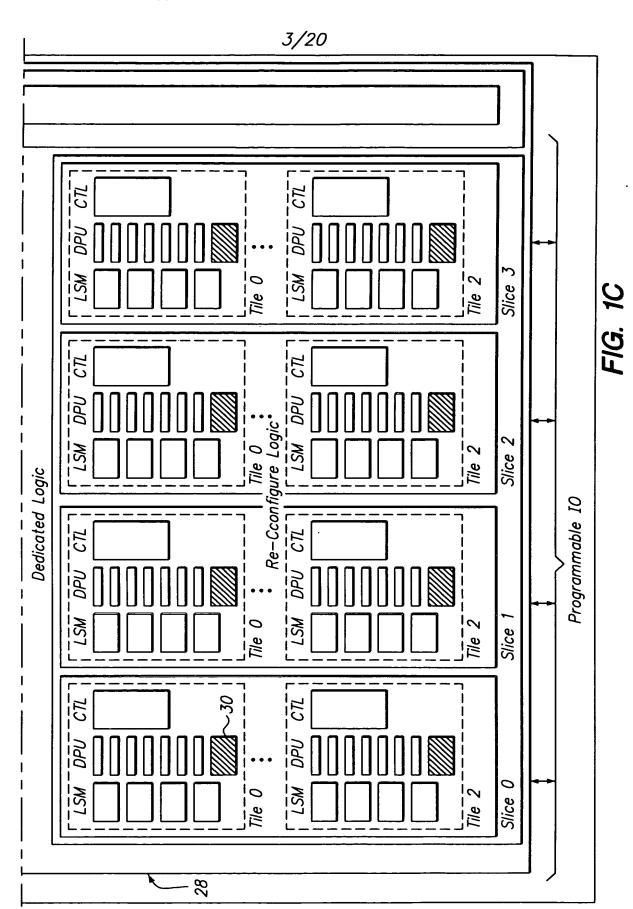
Reconfigurable Chip



Appln. Filing Date: September 19, 200

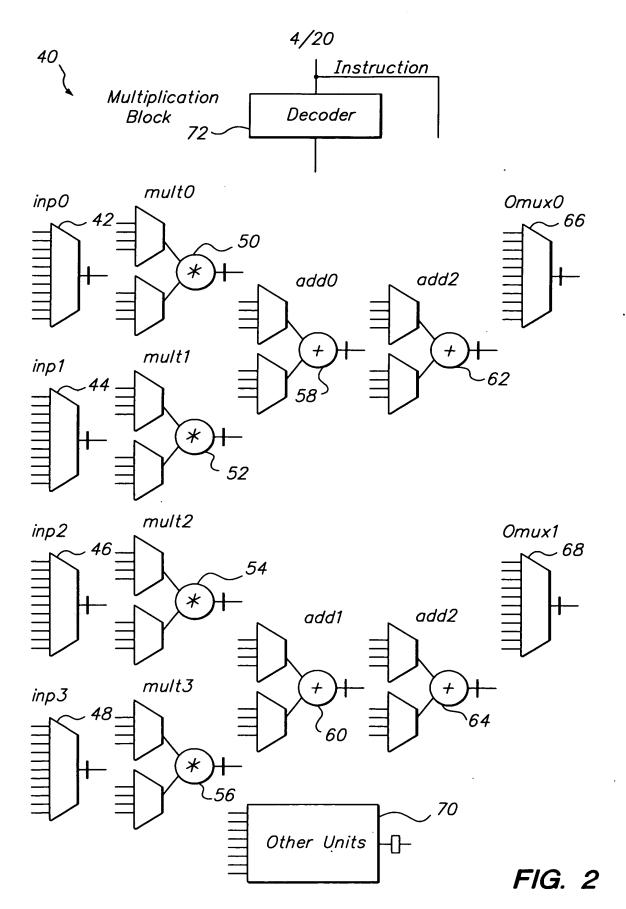
Fitle: Despreader/Correlator Unit for Use in

Reconfigurable Chip



Appln. Filing Date: September 19, 200 Title: Despreader/Correlator Unit for Use in

Reconfigurable Chip

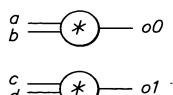


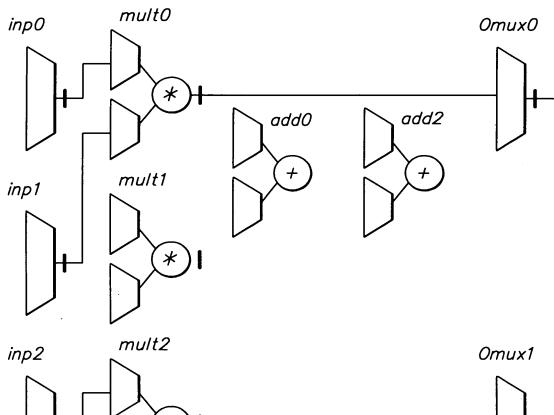
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- 2MULT-CS2112 Compatible mode 2 independent multipliers





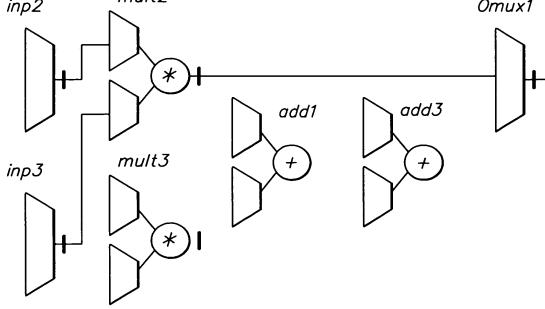


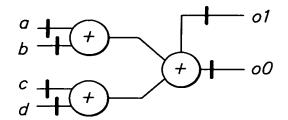
FIG. 3A

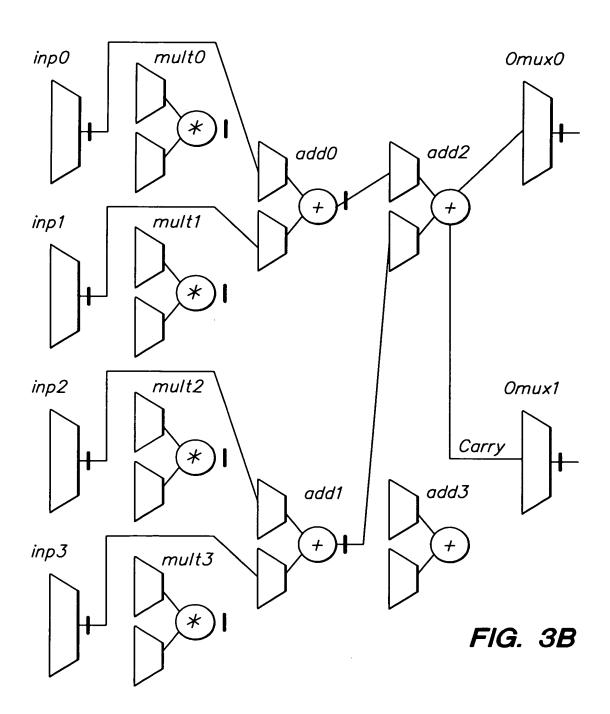
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4ADD32-Sum of 4 32-bit inputs





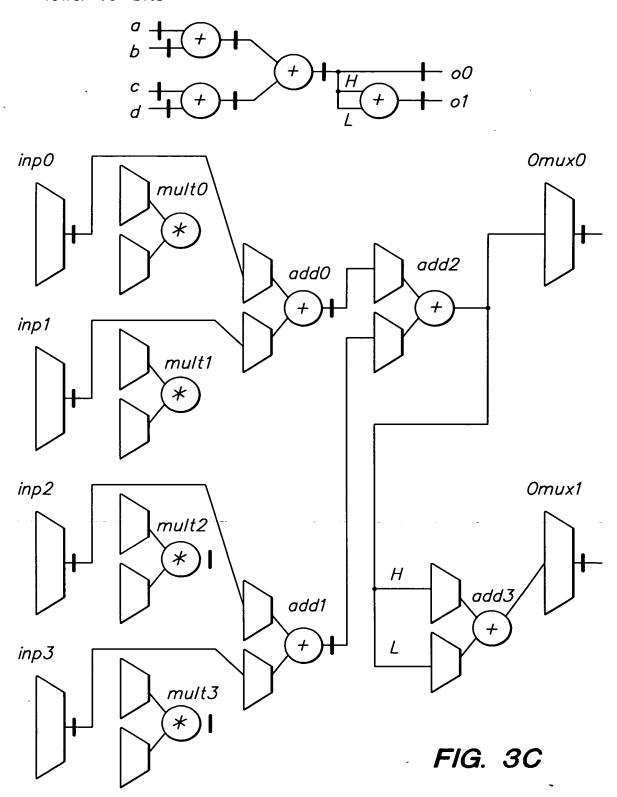
ppln. Filing Date: September 19, 2001 itle: Despreader/Correlator Unit for Use

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4ADD16-Sum of 4 packed 16-bit inputs, sum of upper, lower 16-bits

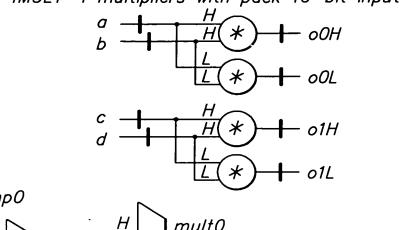


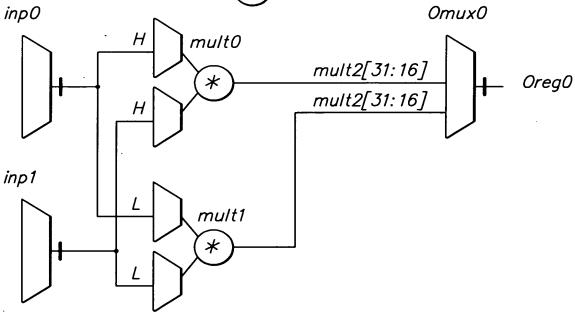
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Let Despreader/Correlator Unit for Use
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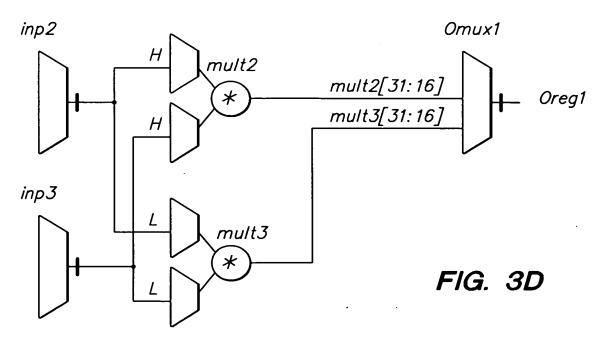
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4MULT-4 multipliers with pack 16-bit inputs





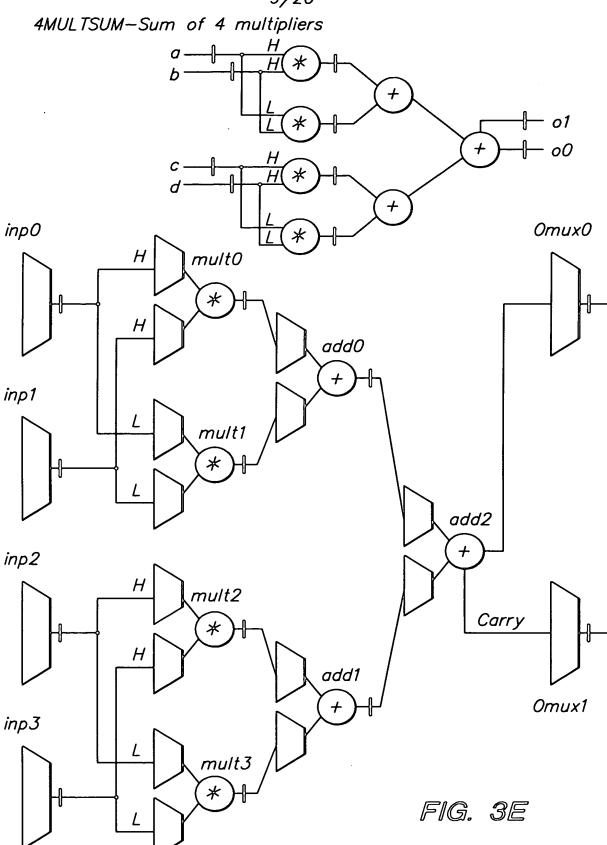


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Application Serial No: 09/960,710

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3



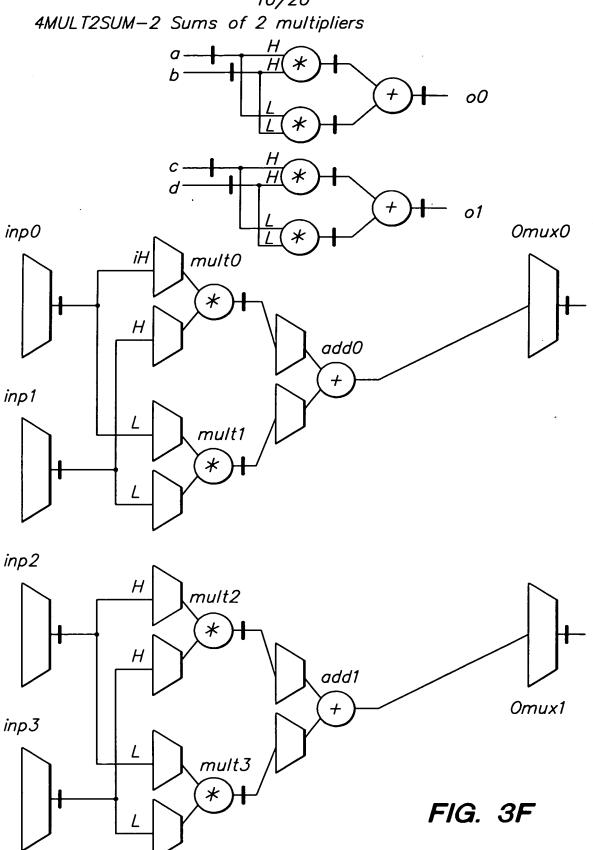
APPLN. FILING DATE: SEPTEMBER 19, 2001

FITLE: DESPREADER/CORRELATOR UNIT

RECONFIGURABLE CHIP USE IN

INVENTOR: BRADLEY L. TAYLOR, ET AL. APPLICATION SERIAL NO: 09/960,710

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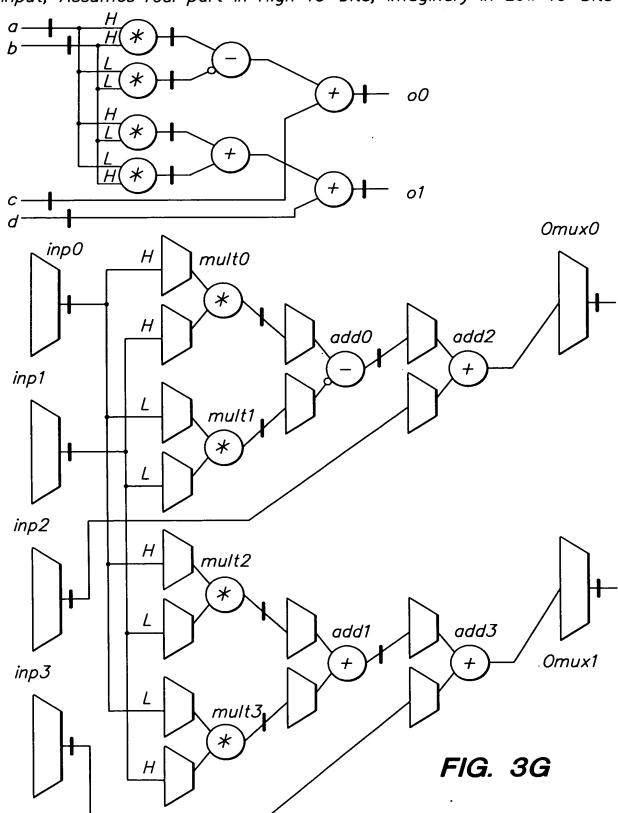
APPLN. FILING DATE: SEPTEMBER 19, 2021
ITLE: DESPREADER/CORRELATOR UNIT USE IN

RECONFIGURABLE CHIP

INVENTOR: BRADLEY L. TAYLOR, ET AL. APPLICATION SERIAL NO: 09/960,710

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CMULT-32-bit output complex multiply with 32-Bit accumulation input, Assumes real part in High 16-bits, imaginary in Low 16-bits



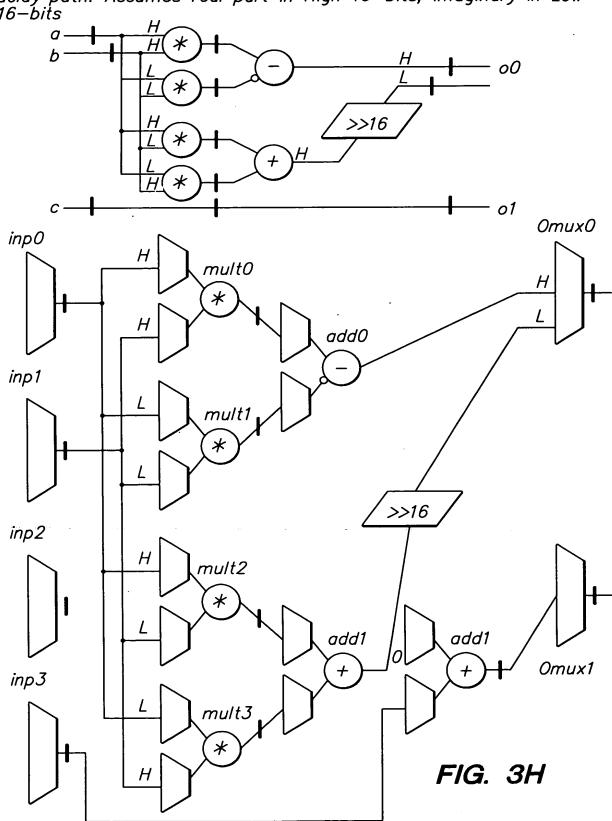
pln. Filing Date: September 19, 2001 Title: Despreader/Correlator Unit for Use in

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CMULT16-Complex Multiplier with 16-Bit Packed data, and indepent delay path. Assumes real part in High 16-bits, imaginary in Low 16-bits

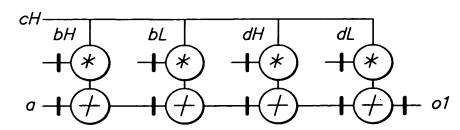


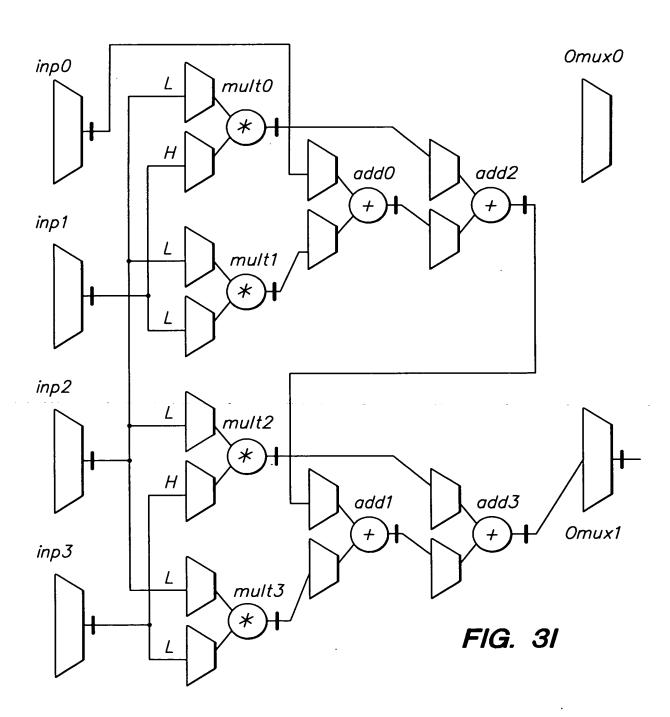
Appln. Filing Date: September 19, 2001
Title: Despreader/Correlator Unit for Us

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Inventor: Bradley L. Taylor, et al.
Application Serial No: 09/960,710

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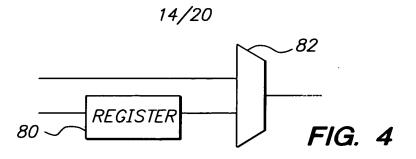
4FIR-4 tap FIR filter

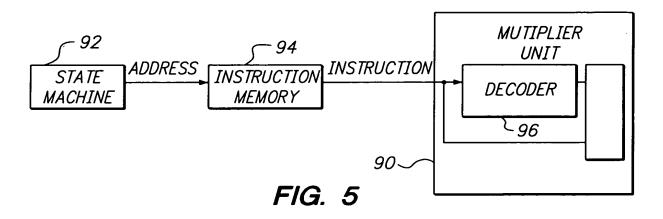


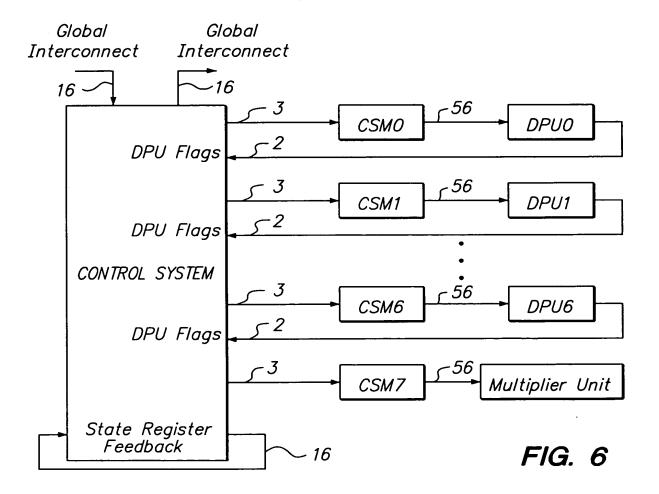


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Fitle: Despreader/Correlator Unit for Unit f

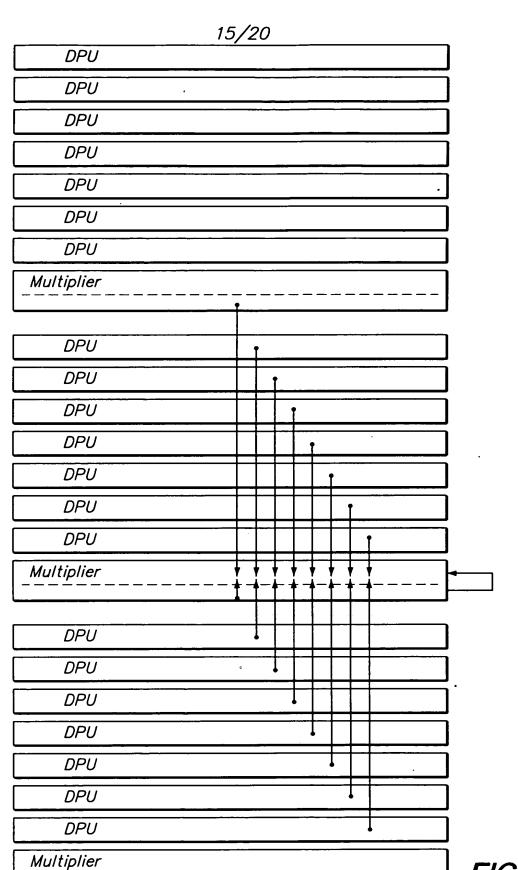






Appln. Filing Date: September 19, 200 Title: Despreader/Correlator Unit for Use in

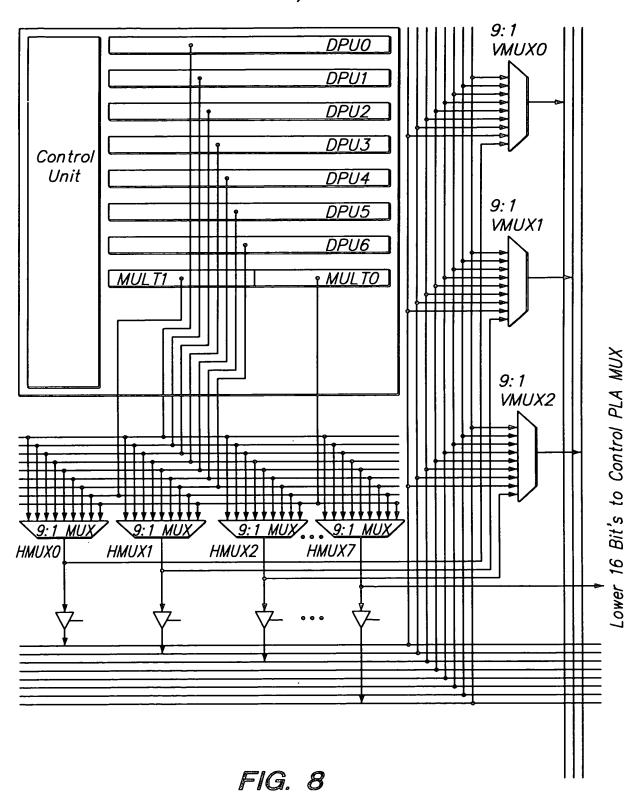
Reconfigurable Chip



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Appln. Filing Date: September 19, 2001)
Title: Despreader/Correlator Unit for Us

Reconfigurable Chip

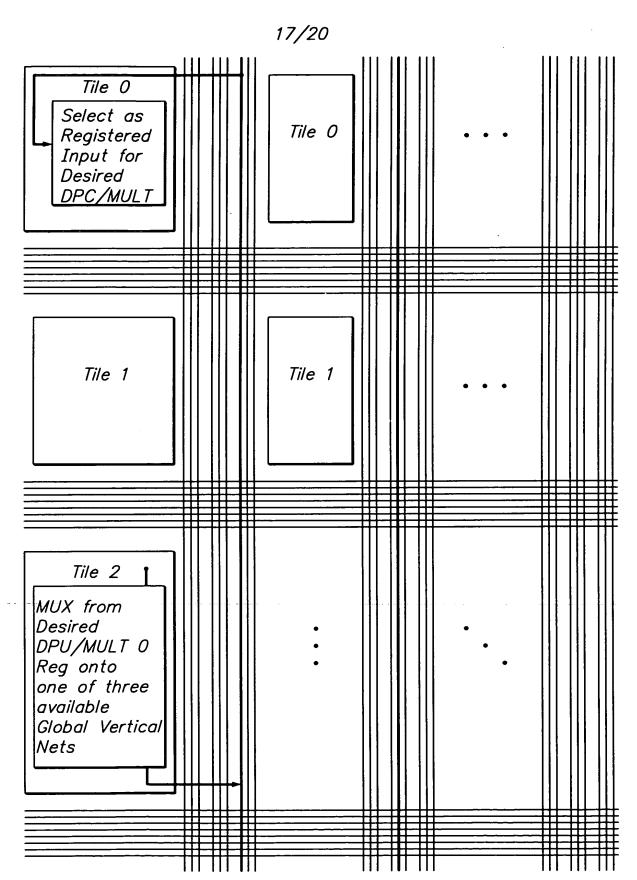


FIG. 9

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Fitle: Despreader/Correlator Unit for Use in Reconfigurable Chip
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Application Serial No: 09/960,710

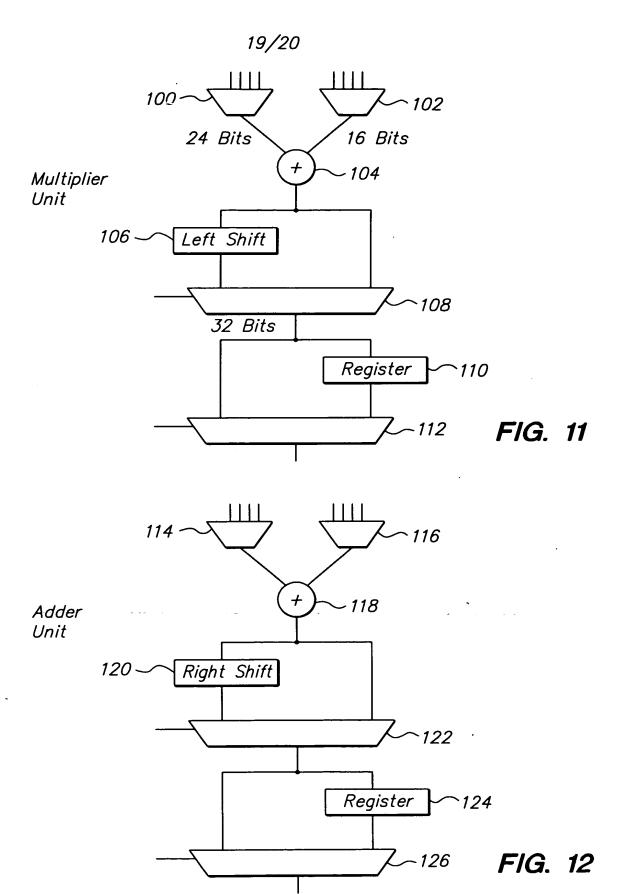
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	Inputmux0 Inputmux0	
	Multmux2 Multmux3	Multmux0 Multmux1
	Mult2	Mult0
	Mult3	Mult1
Other Units	Desp/Corr Tree 0	
	Desp/Corr Tree 1	
	Desp/Corr Tree 2	
	Desp/Corr Tree 3	
	Adder0	
	Adder1	
	Adder2	
	Adder3	
	Outputmux0	
	Outputmux1	

FIG. 10

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Inventor: Bradley L. Taylor, et al.
Application Serial No: 09/960,710



opln. Filing Date: September 19, 2001 ritle: Despreader/Correlator Unit for Use in

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The A and B input muxes select from the following sets of 32-bit signals:

